

FlippingBook Publisher 2
PRODUCT GUIDE

Contents

- 3 **Introduction**
- 4 **Quick Start**
- 13 **System Requirements**

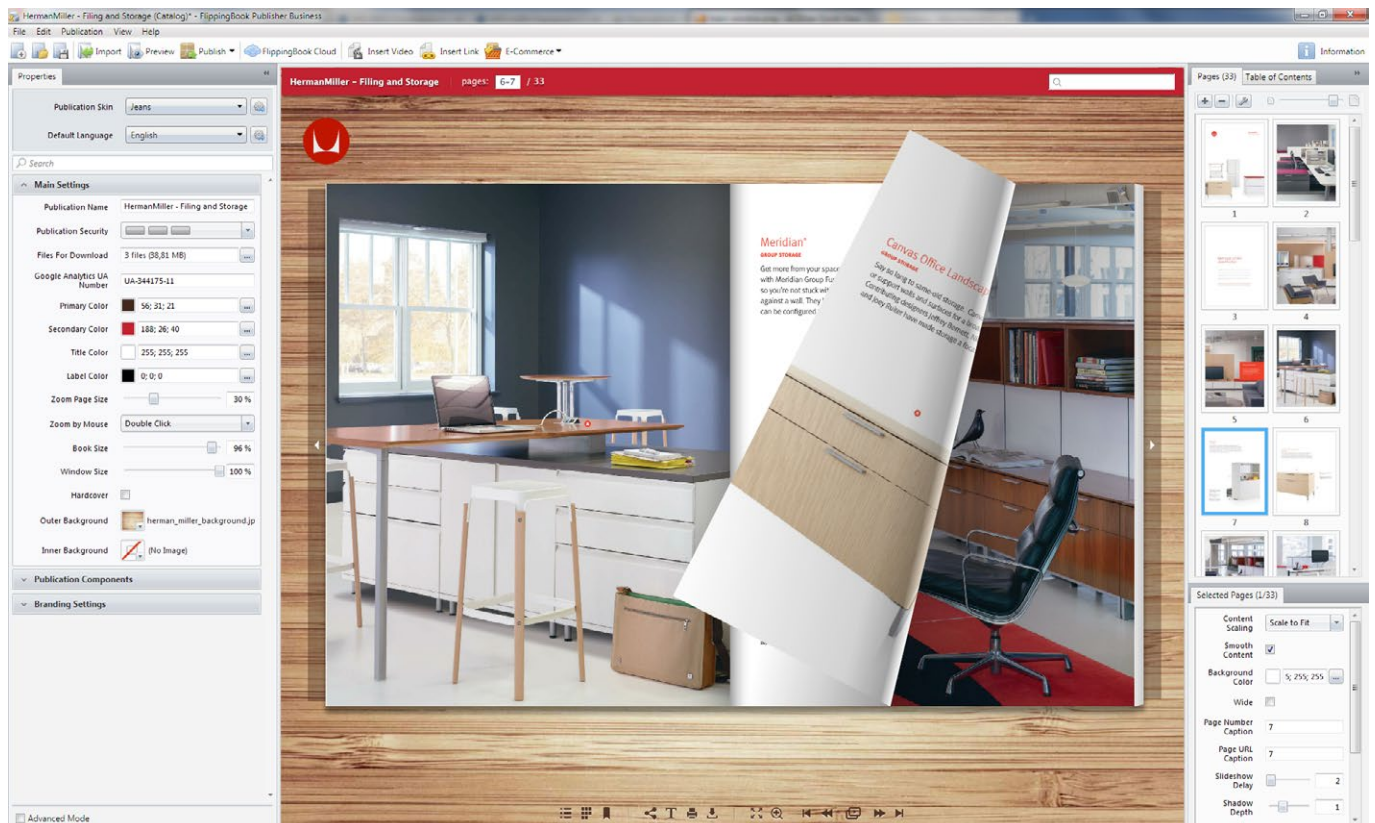
Introduction

Publisher 2 is a Windows application, designed to create modern, user-friendly and attractive publications from traditional documents.

With the help of Publisher 2, you will be able to create online publications for websites, offline publications for CD, DVD and Flash-drives (for Windows and Mac OS X), as well as online publications for mobile devices (iPad, iPhone, and Android devices). PDFs and images (JPEG, PNG, BMP, TIFF and SWF) can be used as source files.

With the Professional and Business editions you can import any documents stored on your computer, including your MS Word, Excel and PowerPoint files, into your publications. Also Publisher's Business edition supports several integrated methods of protecting your publications from unauthorized access and unwanted copying of content.

The new version integrates with popular social networks, supports very large publications (up to several thousand pages), and allows the creation of publications that will be optimized for search engines (SEO) and special publications for the increasingly popular mobile devices.



Features

- High-performance, Low-power AVR[®] 8-bit Microcontroller
- Advanced RISC Architecture
 - 131 Powerful Instructions – Most Single-clock Cycle Execution
 - 32 x 8 General Purpose Working Registers
 - Fully Static Operation
 - Up to 20 MIPS Throughput at 20 MHz
 - On-chip 2-cycle Multiplier
- Nonvolatile Program and Data Memories
 - 128K Bytes of In-System Self-Programmable Flash
Endurance: 10,000 Write/Erase Cycles
 - Optional Boot Code Section with Independent Lock Bits
In-System Programming by On-chip Boot Program
True Read-While-Write Operation
 - 4K Bytes EEPROM
Endurance: 100,000 Write/Erase Cycles
 - 16K Bytes Internal SRAM
 - Programming Lock for Software Security
- JTAG (IEEE std. 1149.1 Compliant) Interface
 - Boundary-scan Capabilities According to the JTAG Standard
 - Extensive On-chip Debug Support
 - Programming of Flash, EEPROM, Fuses, and Lock Bits through the JTAG Interface
- Peripheral Features
 - Two 8-bit Timer/Counters with Separate Prescalers and Compare Modes
 - Two 16-bit Timer/Counter with Separate Prescaler, Compare Mode, and Capture Mode
 - Real Time Counter with Separate Oscillator
 - Six PWM Channels
 - 8-channel, 10-bit ADC
Differential mode with selectable gain at 1x, 10x or 200x
 - Byte-oriented Two-wire Serial Interface
 - Two Programmable Serial USART
 - Master/Slave SPI Serial Interface
 - Programmable Watchdog Timer with Separate On-chip Oscillator
 - On-chip Analog Comparator
 - Interrupt and Wake-up on Pin Change
- Special Microcontroller Features
 - Power-on Reset and Programmable Brown-out Detection
 - Internal Calibrated RC Oscillator
 - External and Internal Interrupt Sources
 - Six Sleep Modes: Idle, ADC Noise Reduction, Power-save, Power-down, Standby and Extended Standby
- I/O and Packages
 - 32 Programmable I/O Lines
 - 40-pin PDIP, 44-lead TQFP, and 44-pad QFN/MLF
- Operating Voltages
 - 1.8 - 5.5V for ATmega1284P
- Speed Grades
 - 0 - 4 MHz @ 1.8 - 5.5V
 - 0 - 10 MHz @ 2.7 - 5.5V
 - 0 - 20 MHz @ 4.5 - 5.5V
- Power Consumption at 1 MHz, 1.8V, 25°C
 - Active: 0.4 mA
 - Power-down Mode: 0.1 µA
 - Power-save Mode: 0.7 µA (Including 32 kHz RTC)



8-bit AVR[®] Microcontroller with 128K Bytes In-System Programmable Flash

ATmega1284P

Preliminary

